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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM FILING DATE CLASS SUBCLASS GAU EXAMINER
10014243 12/11/2001 257 204 2811 MALLON/HOFMANN

**APPLICANTS: Van Den Heuvel Renerus; 2823

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:
EUROPEAN PATENT OFFICE (EPO) 00204429.5 12/11/2000

PG-PUB DO NOT PUBLISH ☐ RESCIND ☐
Foreign priority claimed ☐ yes ☐ no
35 USC 119 conditions met ☐ yes ☐ no
Verified and Acknowledged Examiners's initials
ATTORNEY DOCKET NO
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TITLE : Method for the manufacture of a semiconductor device with a field-effect transistor

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
ISSUE FEE			Total Claims	Print Claims for O.G.
Amount Due	Date Paid	Primary Examiner	DRAWING	
			Sheets Drawg.	Figs. Drawg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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